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Silicon N Channel Power MOS FET High Speed Power Switching

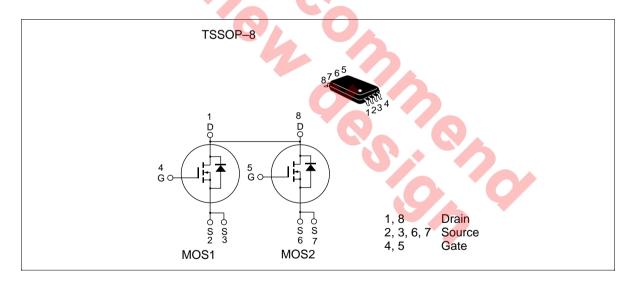


ADE-208-669F (Z) 7th. Edition Feb. 1999

#### **Features**

- Low on-resistance
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting

#### **Outline**



## **Absolute Maximum Ratings** $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	28	V
Gate to source voltage	$V_{\sf GSS}$	± 12	V
Drain current	I <sub>D</sub>	5.0	A
Drain peak current	Note1 D(pulse)	40	A
Body-drain diode reverse drain current	I <sub>DR</sub>	5.0	A
Channel dissipation	Pch Note2	1.0	W
Channel dissipation	Pch Note3	1.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	- 55 to + 150	°C

Note: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

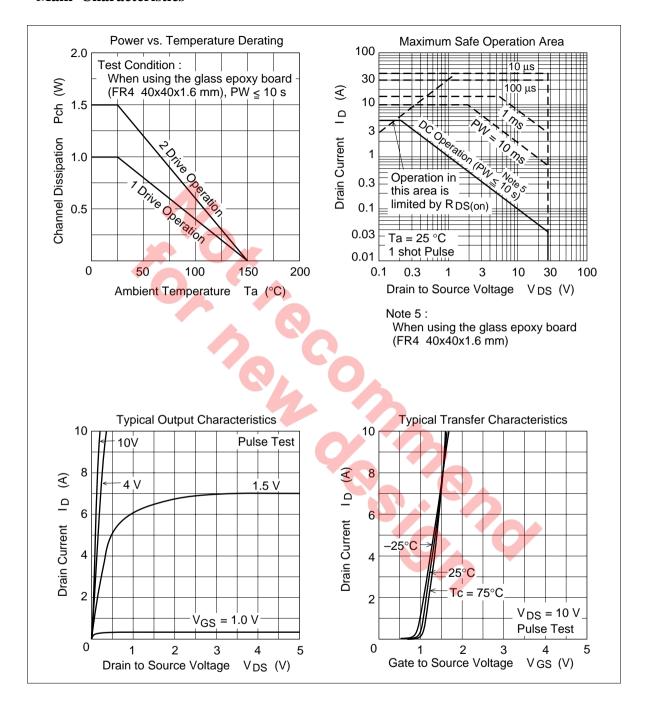
- 2. 1 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s
- 3. 2 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s

### **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

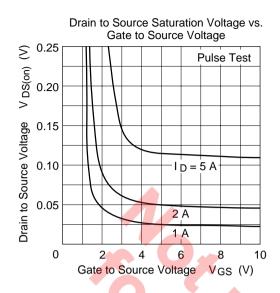
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	28		_	V	$I_{D} = 10 \text{mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>			± 0.1	μΑ	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	70	_ <	1	μΑ	$V_{DS} = 28 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.4	= .	1.4	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	- (	0.027	0.034	Ω	$I_D = 3 A$ , $V_{GS} = 4 V^{Note4}$
resistance	R <sub>DS(on)</sub>	_ `	0.037	0.044	Ω	$I_D = 3 \text{ A}, V_{GS} = 2.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	7	11	<del>-</del>	S	$I_D = 3 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	510	U	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	190	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	140	- (	pF	f = 1 MHz
Total gate charge	Qg	_	8.5	_	nc	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	4.5	_	nc	$V_{GS} = 4 V$
Gate to drain charge	Qgd	_	4	_	nc	I <sub>D</sub> = 5 A
Turn-on delay time	t <sub>d(on)</sub>	_	14	_	ns	$V_{GS} = 4 \text{ V}, I_D = 3 \text{ A}$
Rise time	t <sub>r</sub>	_	120	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t <sub>d(off)</sub>	_	85	_	ns	<del>-</del>
Fall time	t <sub>f</sub>	_	120	_	ns	_
Body-drain diode forward voltage	$V_{DF}$	_	0.85	1.1	V	$IF = 5.0 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	40	_	ns	IF = 5.0 A, $V_{GS} = 0$ diF/ dt = 20 A/ $\mu$ s
Notes 4 Dules test						

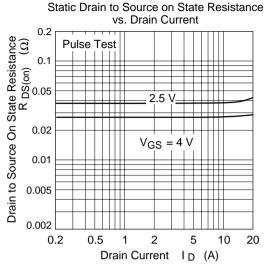
Note: 4. Pulse test

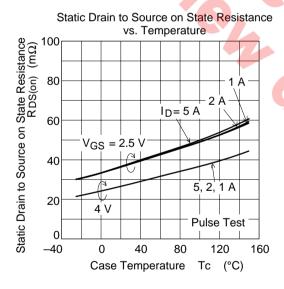
#### **Main Characteristics**

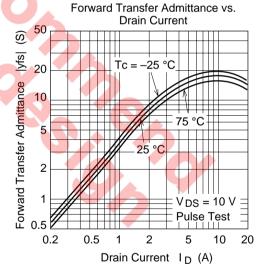


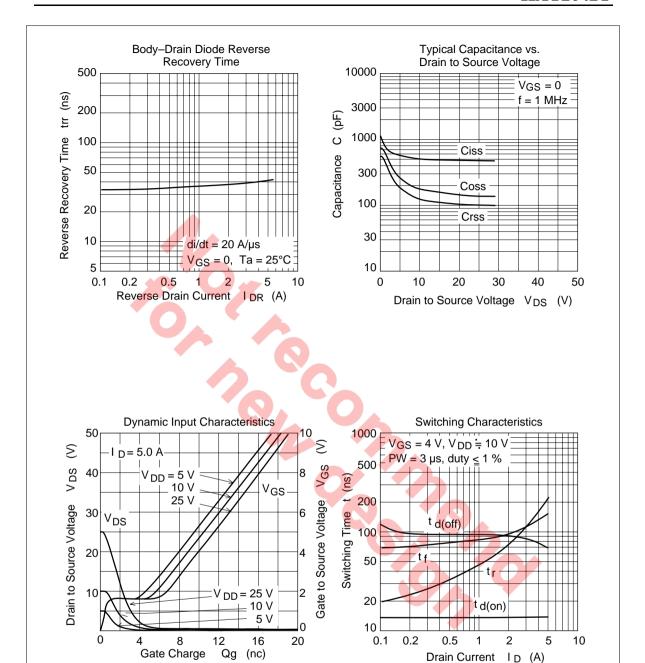
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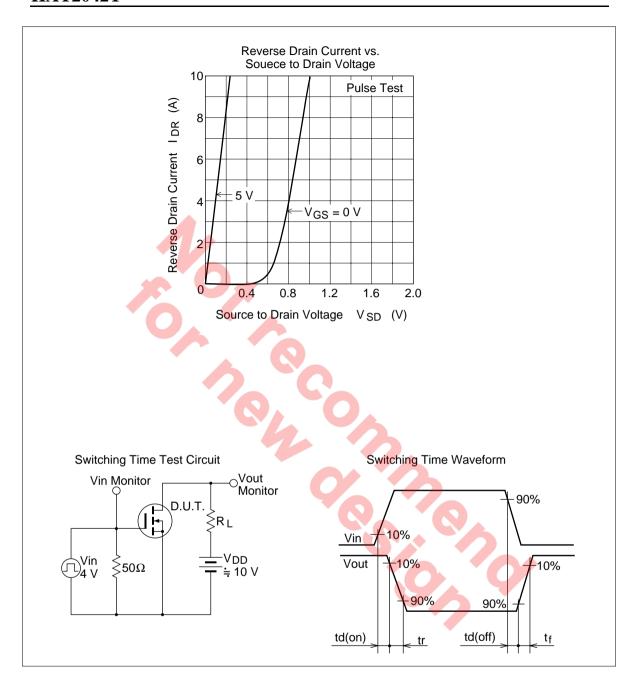


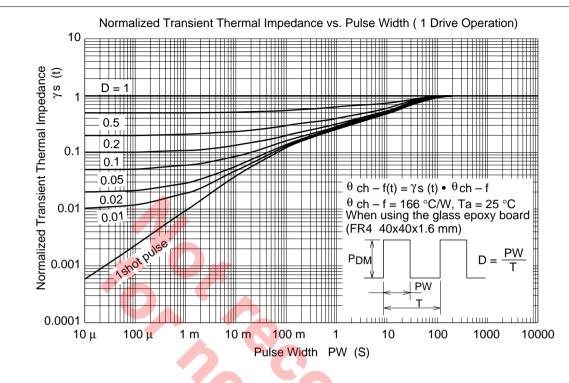


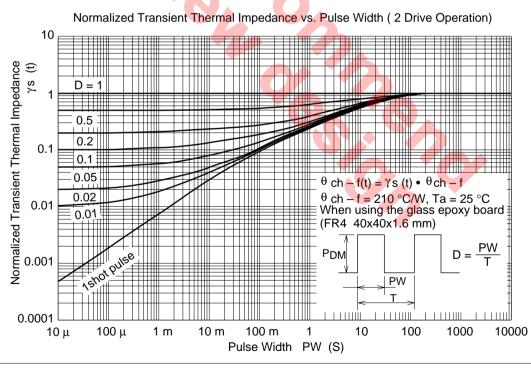




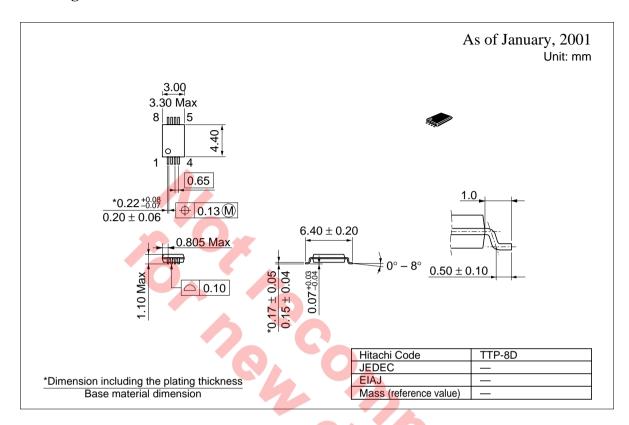








## **Package Dimensions**



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